

008280" 62564960

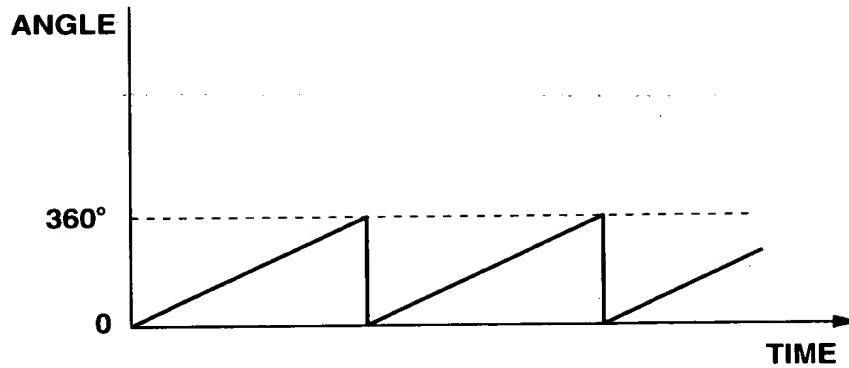


FIG.1

- LPF INPUT PHASE AND CORRECT OUTPUT PHASE (FOR A RANGE OF FROM 0° TO 360°)
- - - LPF INPUT PHASE AND CORRECT OUTPUT PHASE (WITHOUT LIMITATION TO RANGE)
- NORMAL LPF OUTPUT PHASE (FOR A RANGE OF FROM 0° TO 360°)

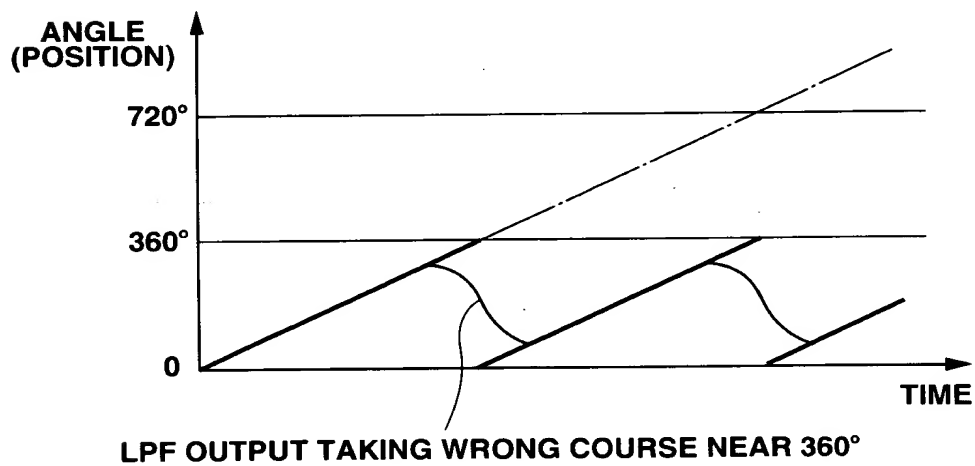


FIG.2

009280" 62564960

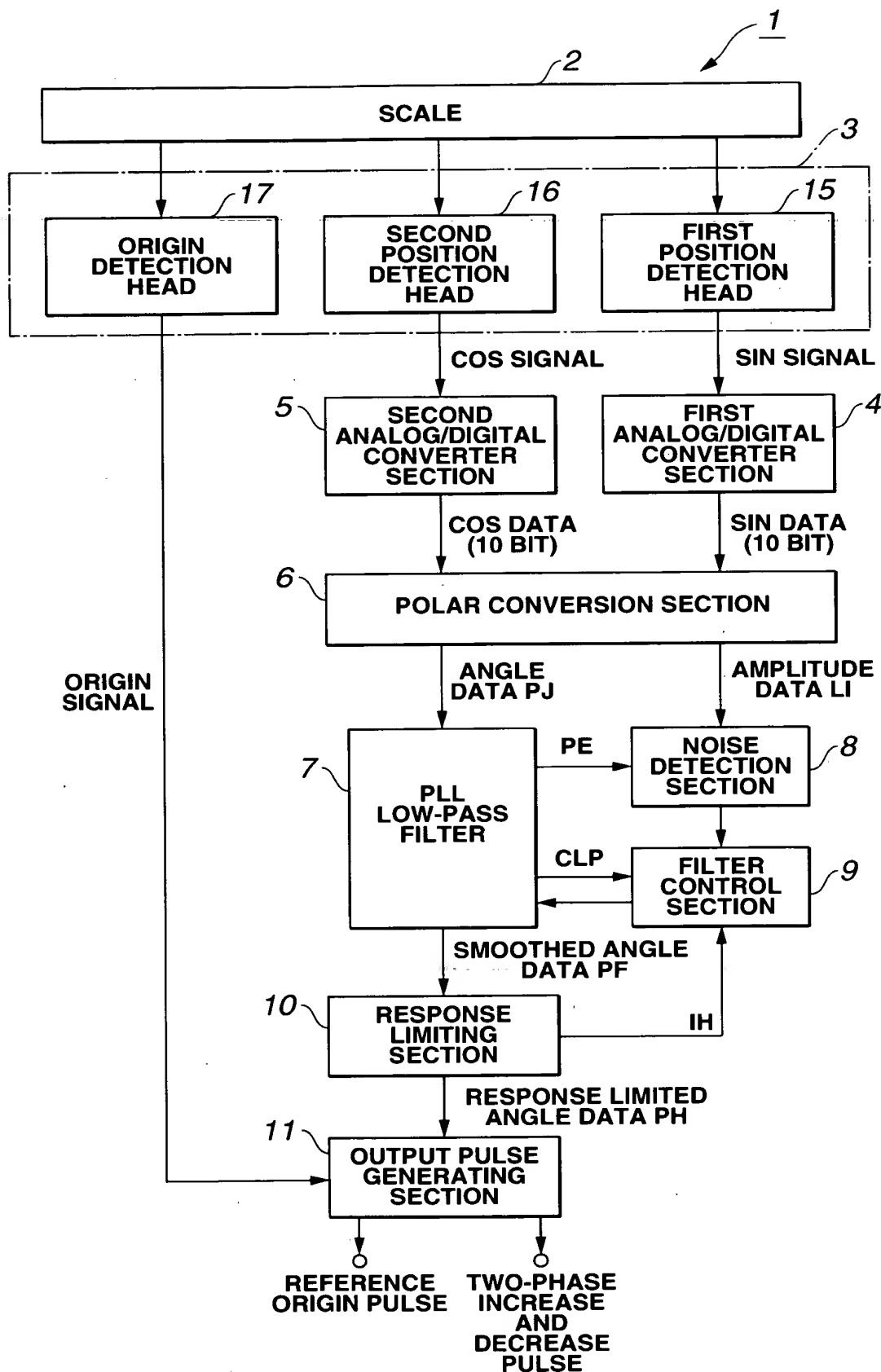


FIG.3

FIG.4A

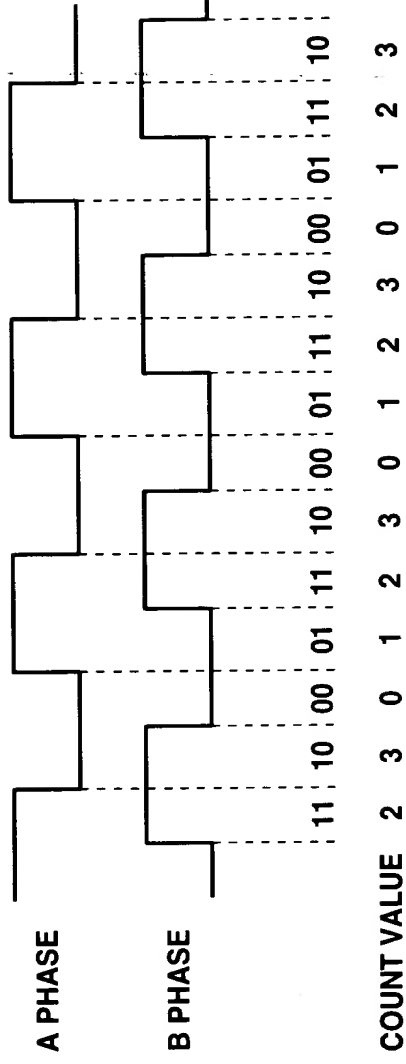


FIG.4B

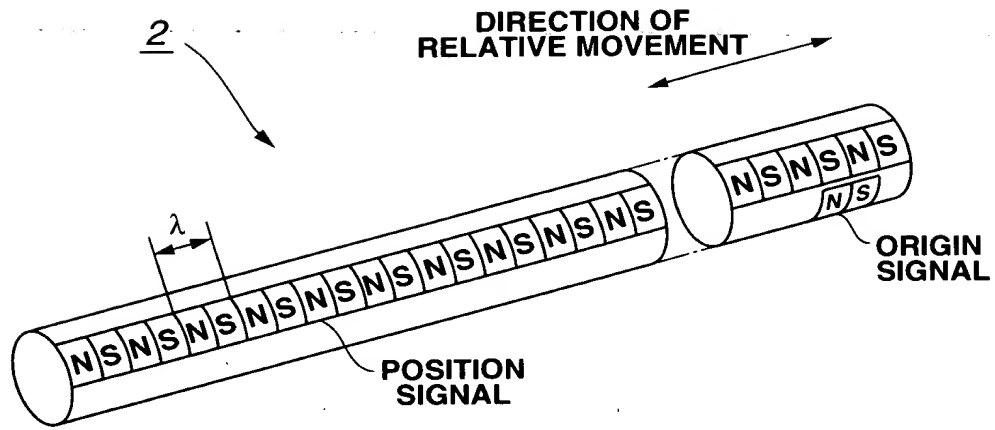


FIG. 5

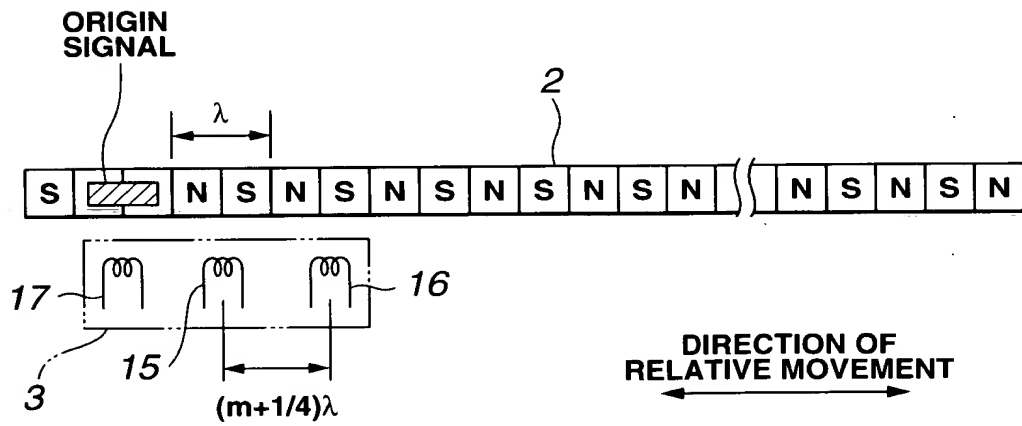


FIG. 6

008280" 6E564960

FIG.7A

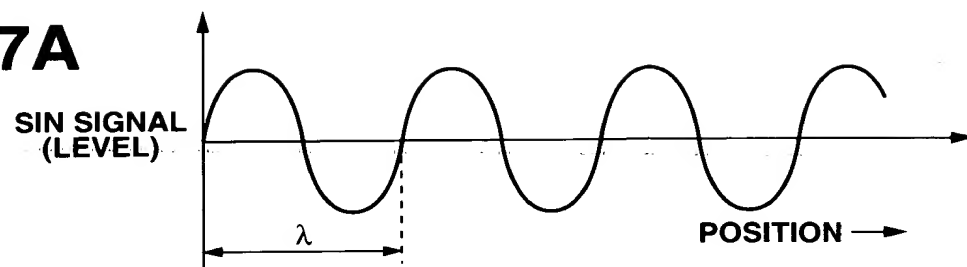


FIG.7B

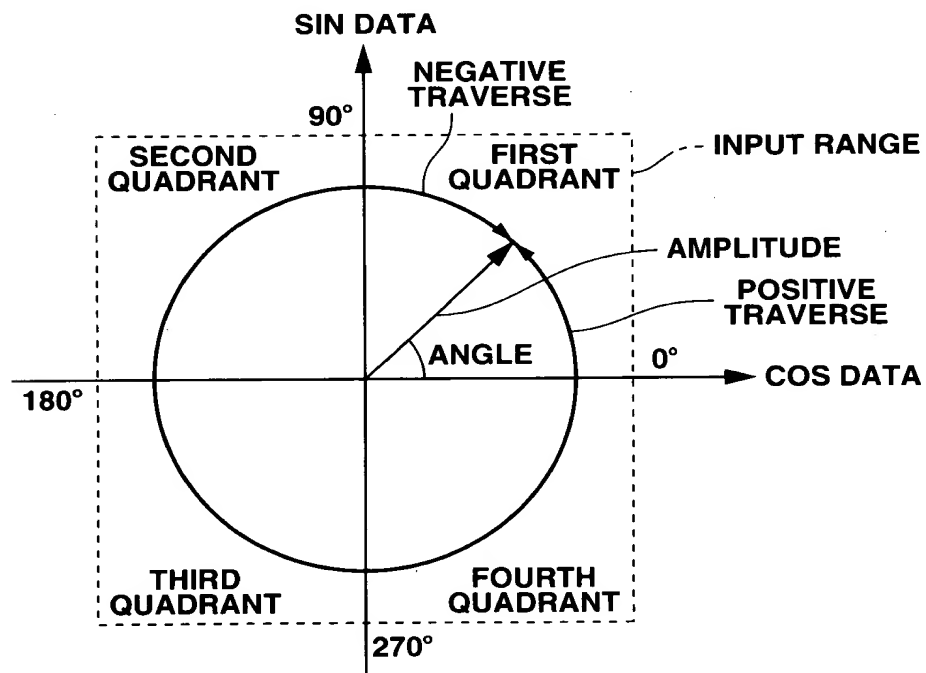
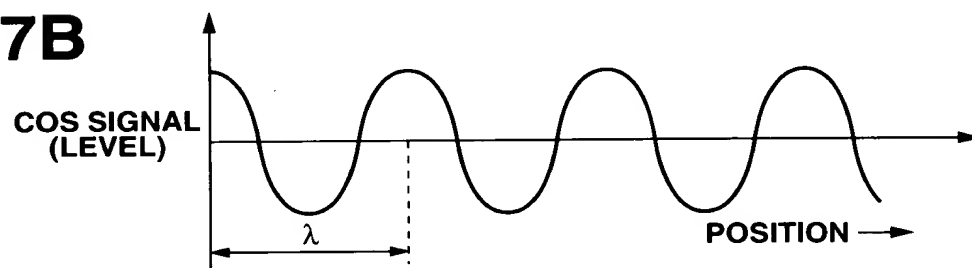


FIG.8

FIG.9

09649539 082800

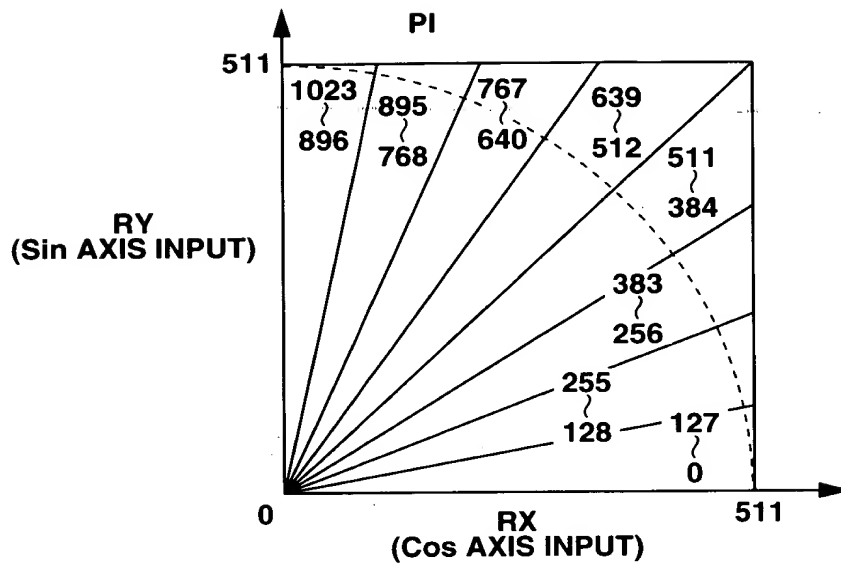


FIG.10

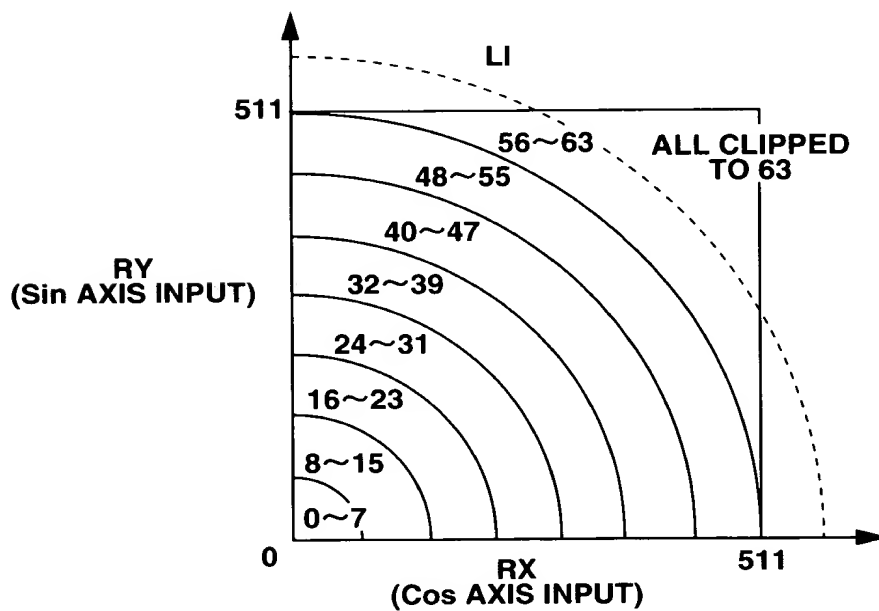


FIG.11

008280" 62564960

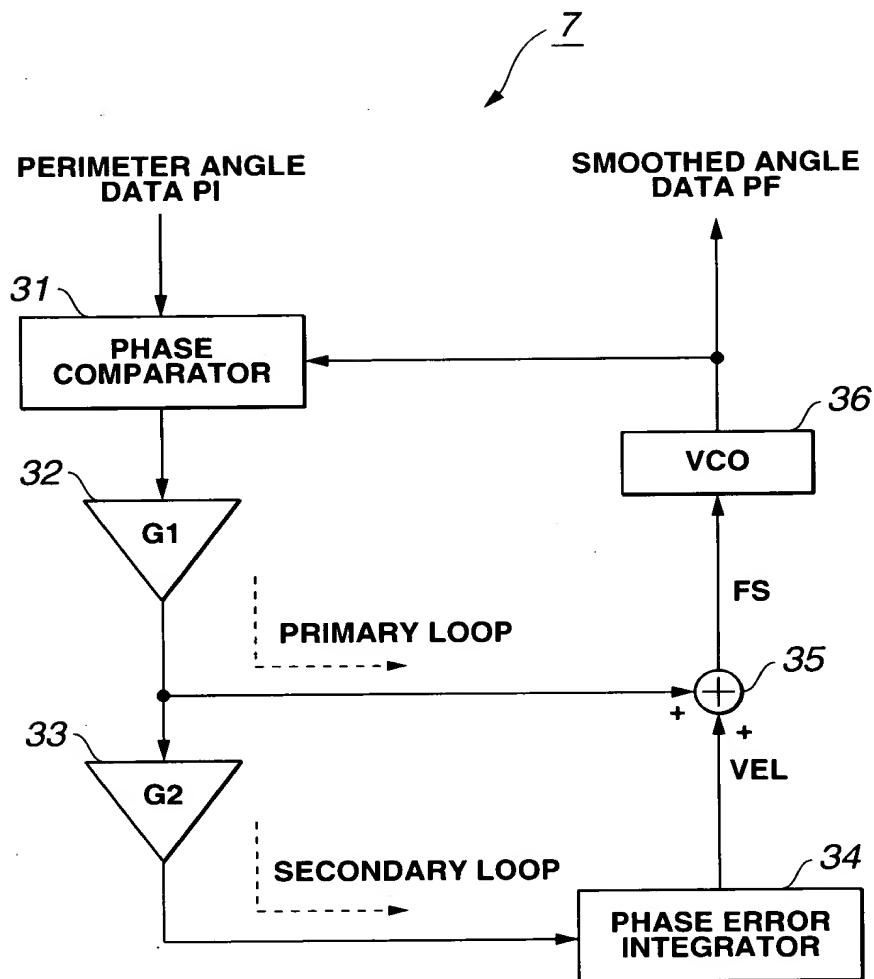


FIG.12

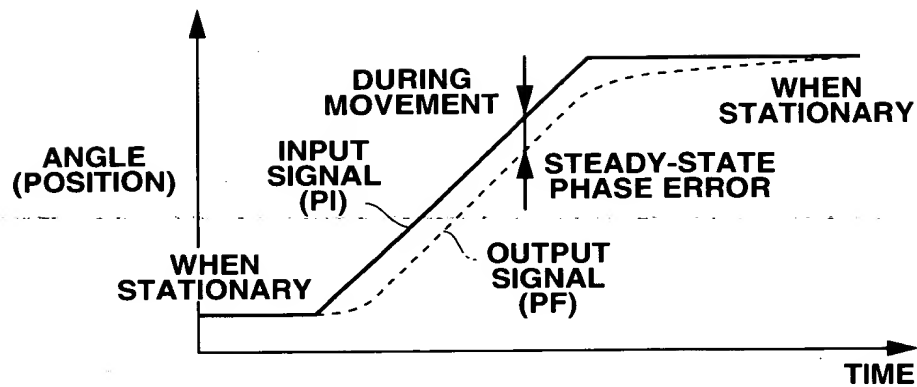


FIG.13

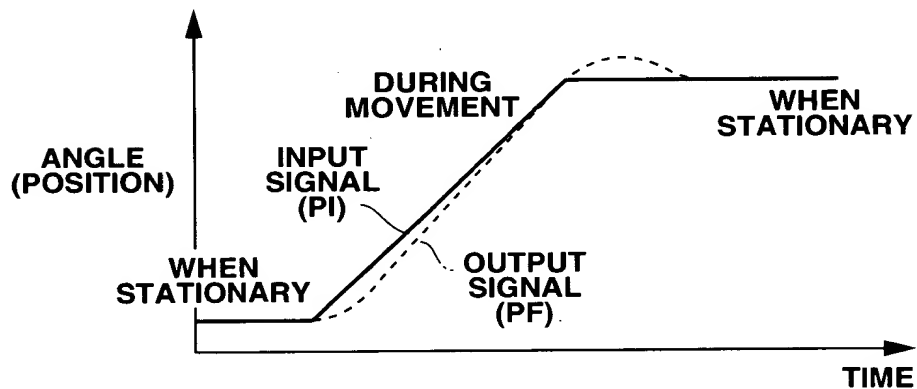


FIG.14

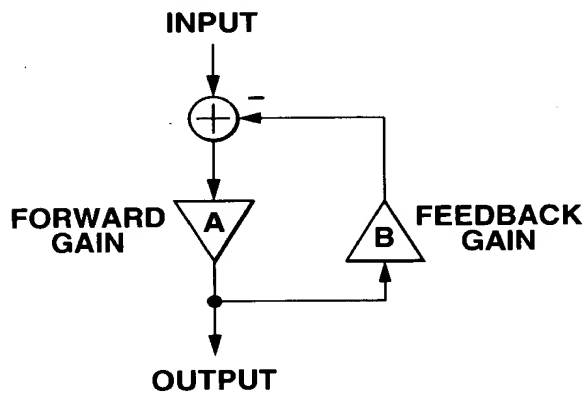


FIG.15

008280" 6E564960

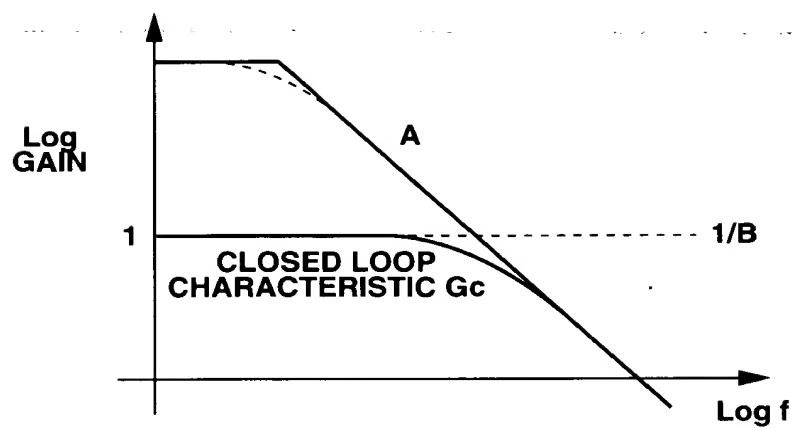


FIG.16

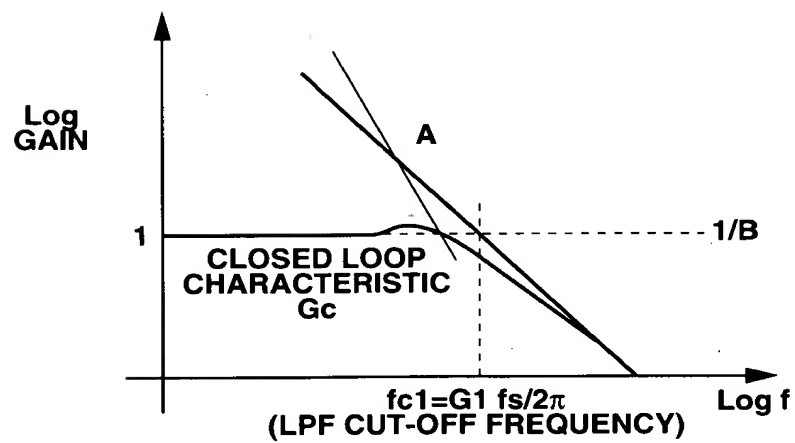


FIG.17

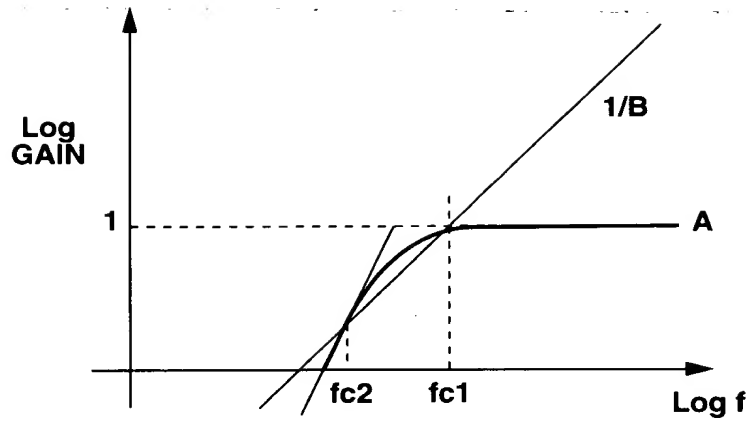


FIG.18

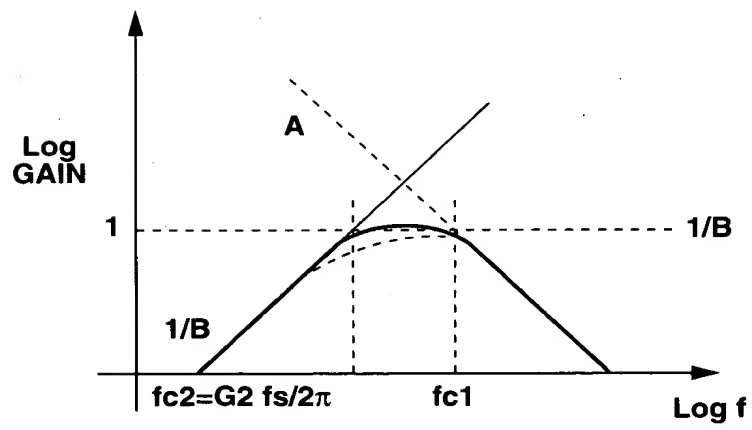


FIG.19

008280* 6554960

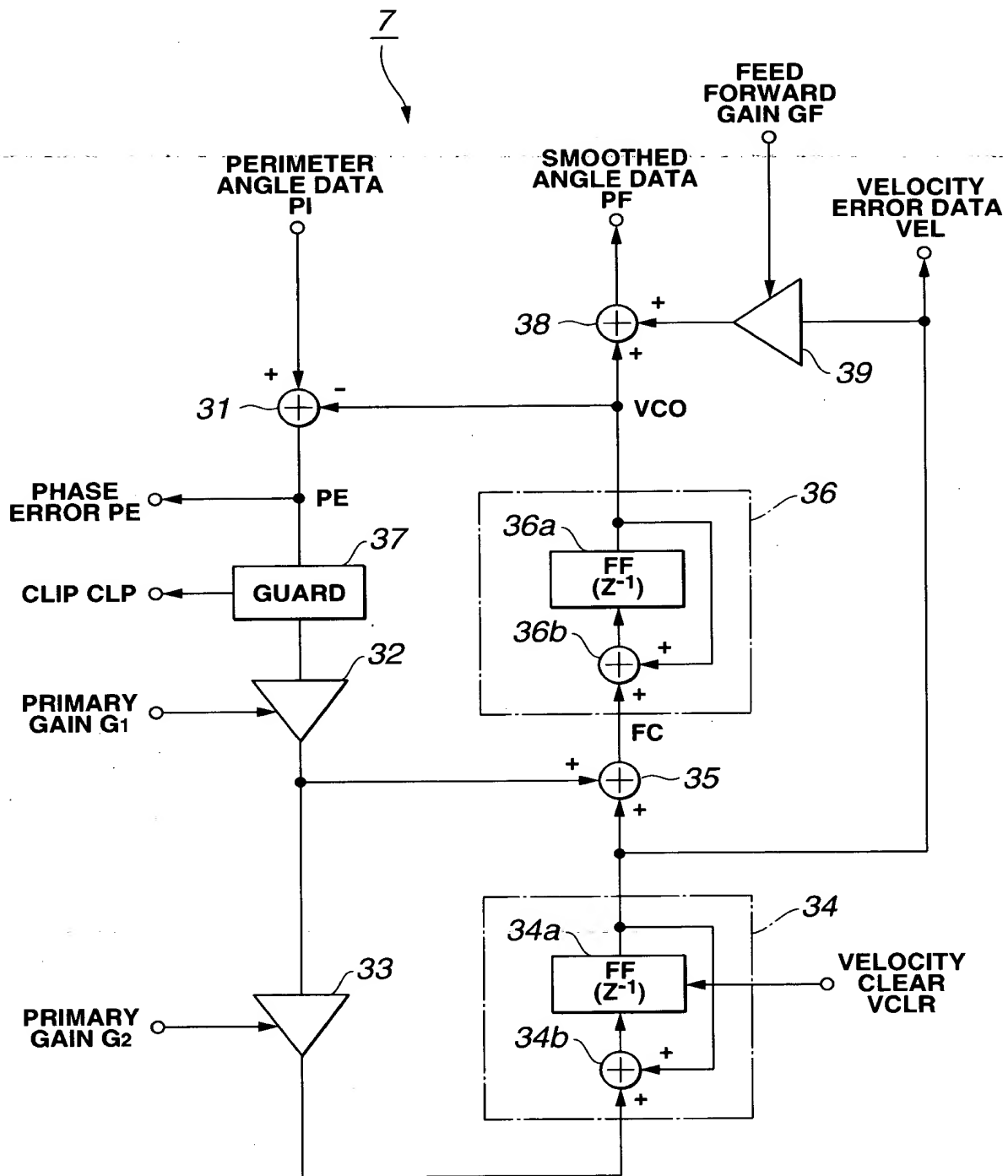


FIG.20

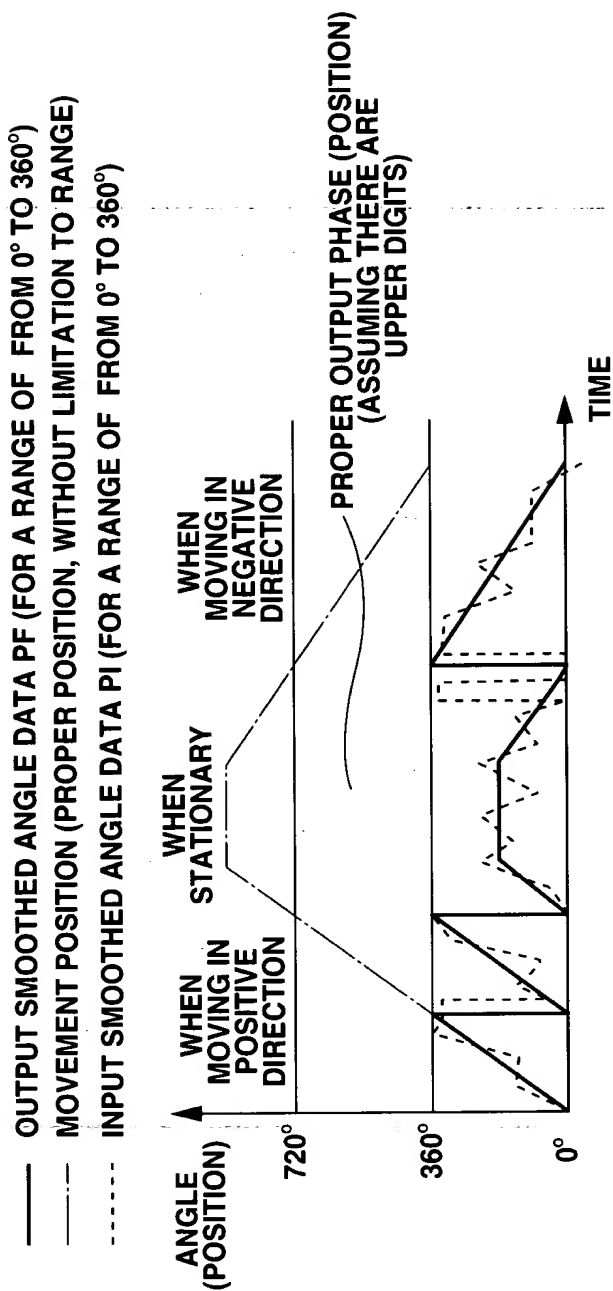


FIG.21A

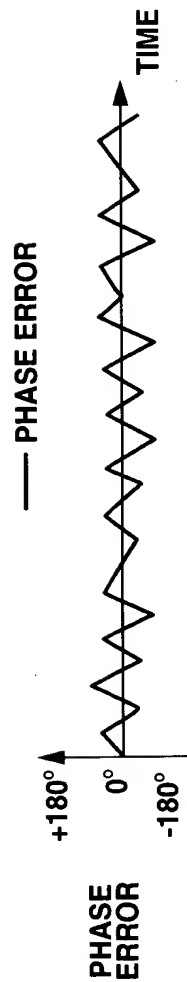


FIG.21B

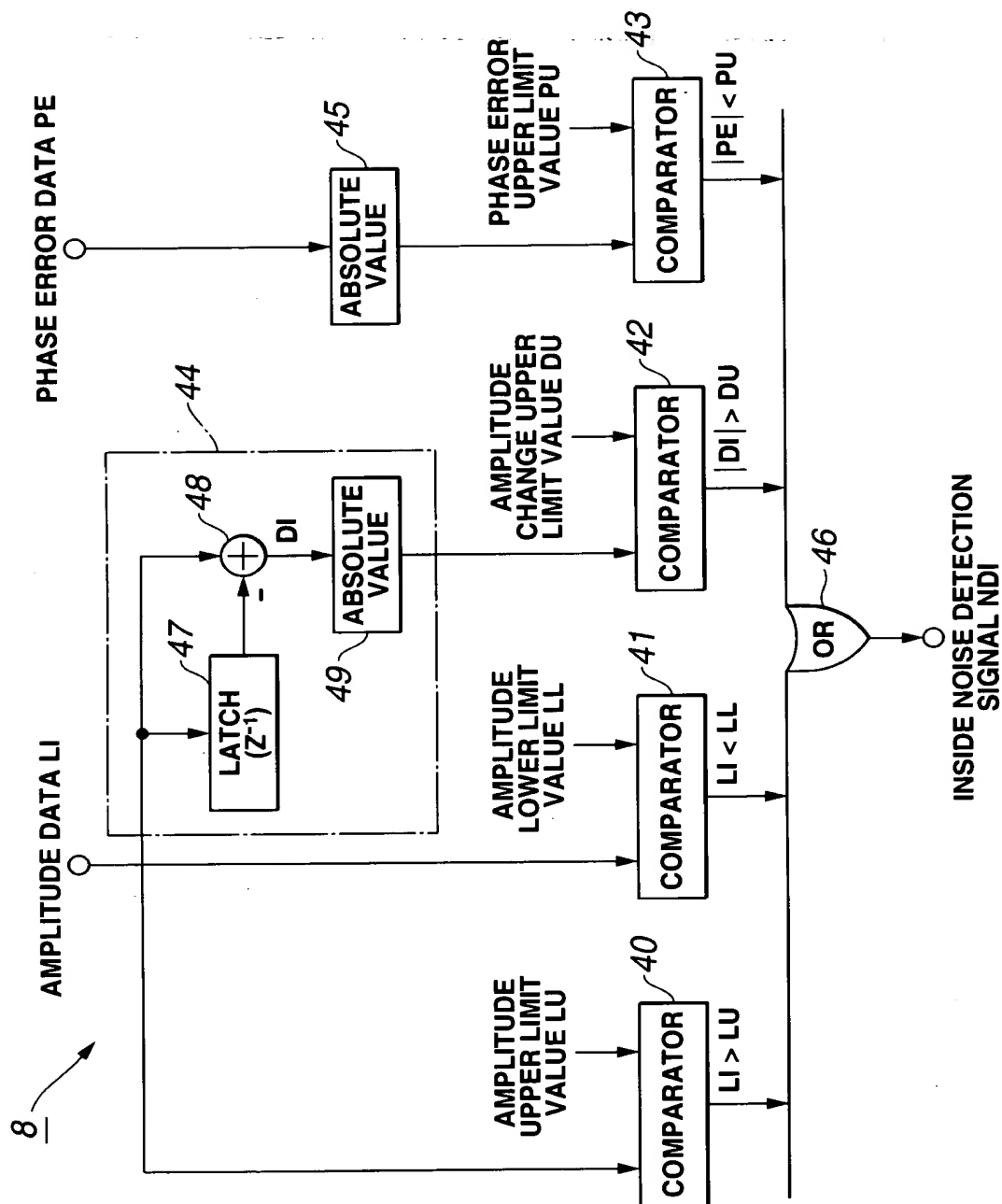


FIG.22

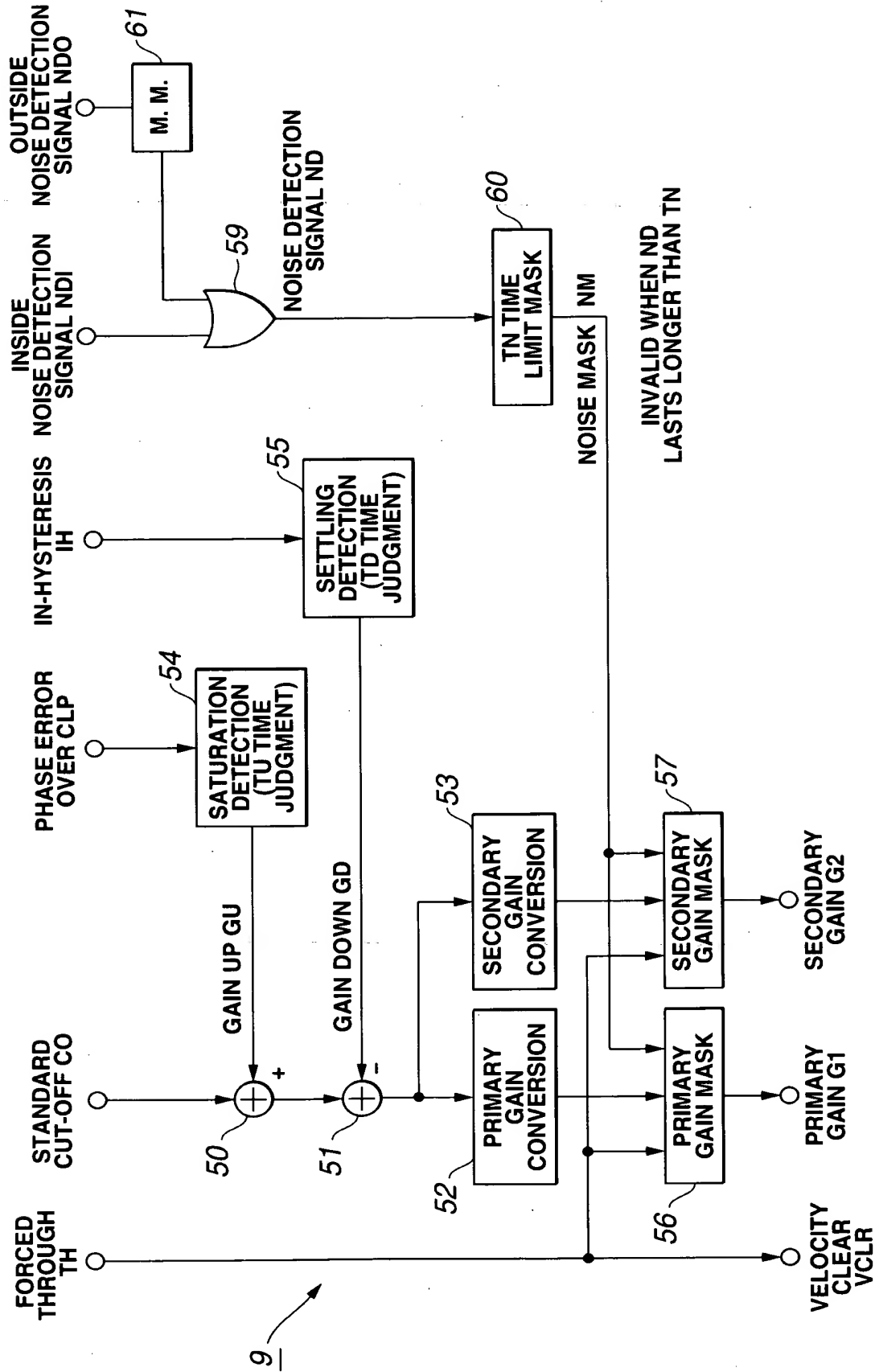
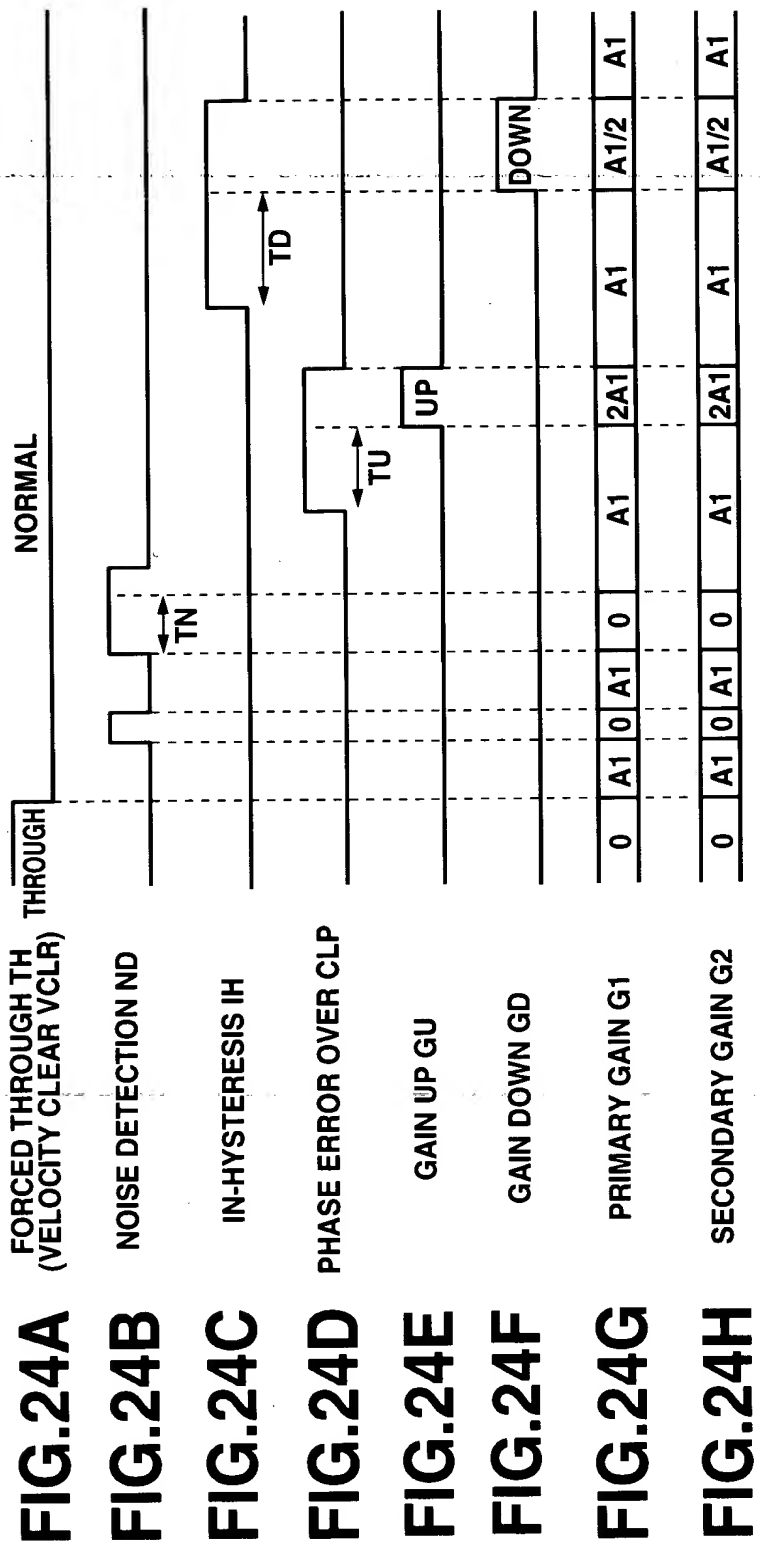


FIG.23



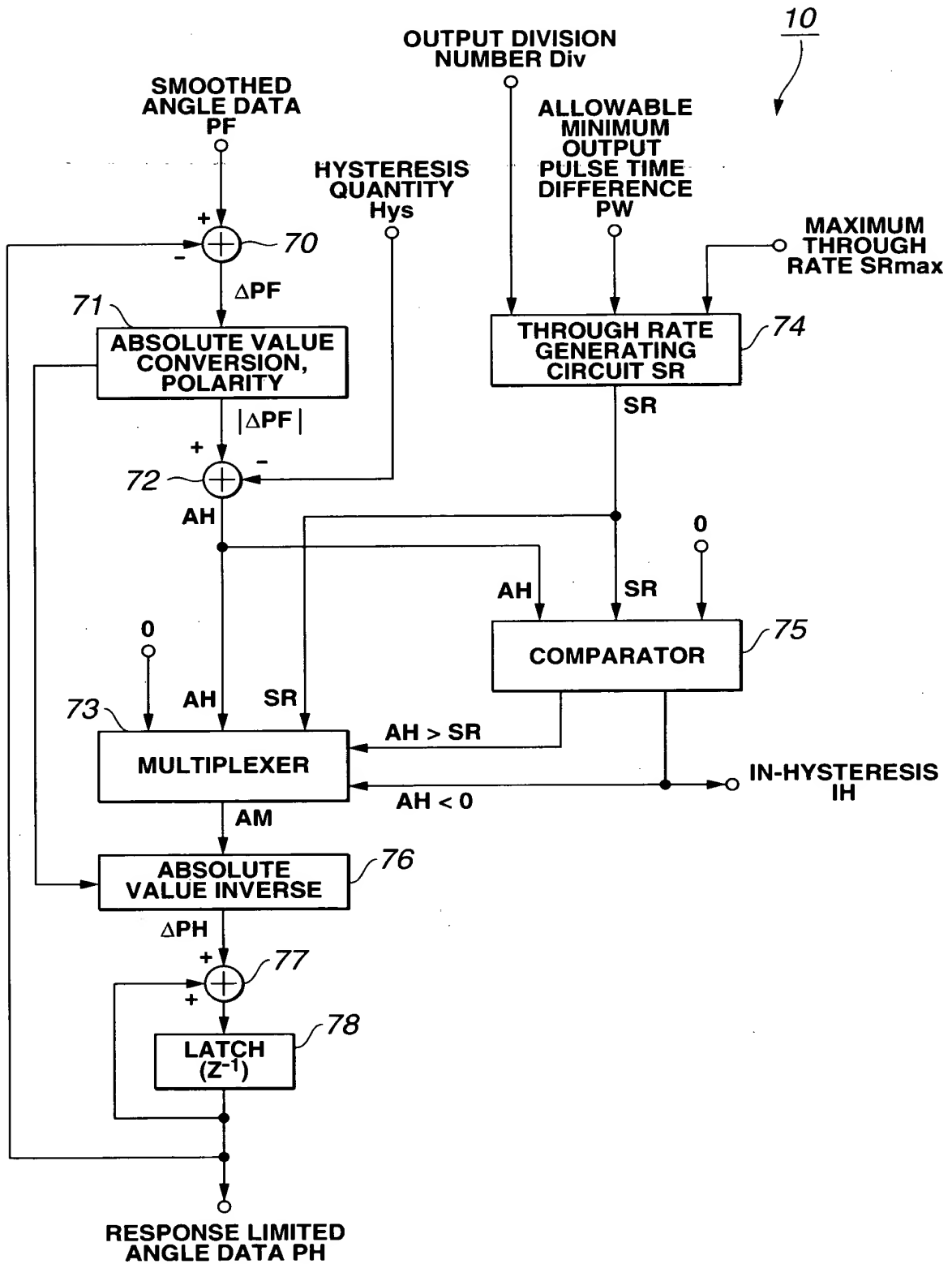


FIG.25

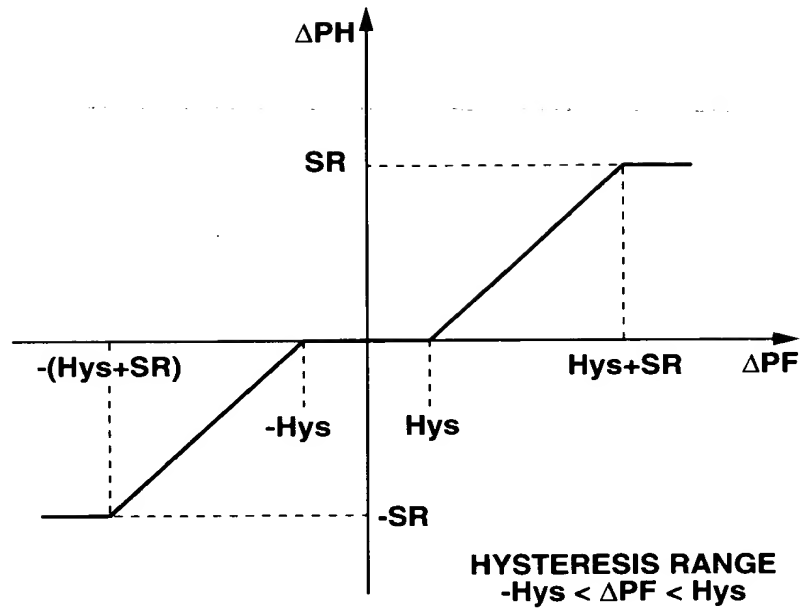


FIG.26

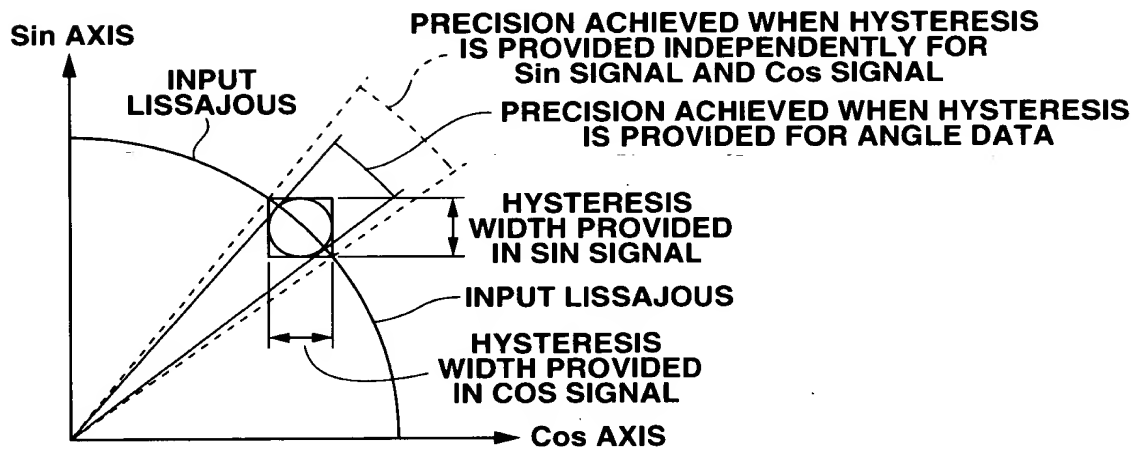


FIG.27

002280" 65564960

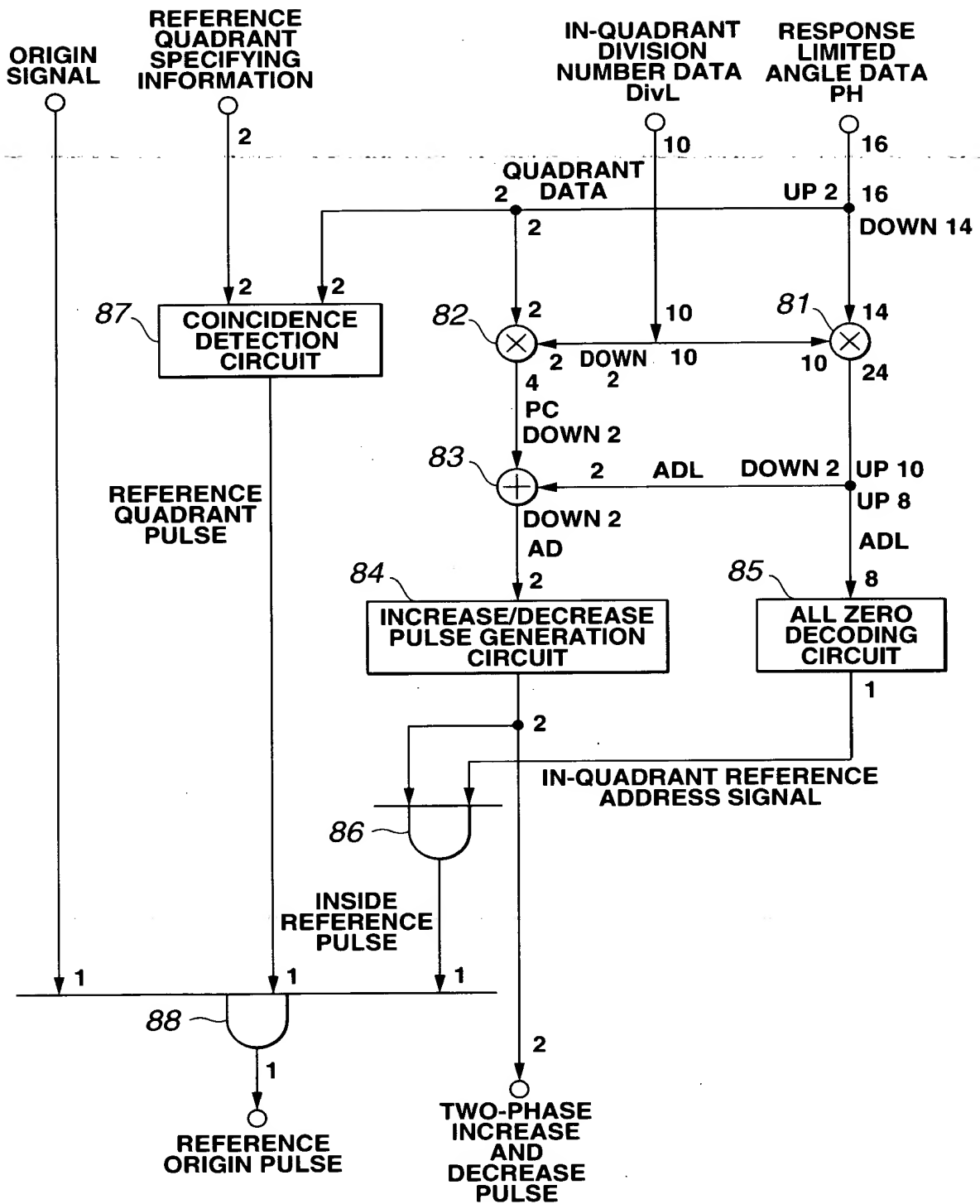


FIG.28

FIG.29A

20	21	22	23	24	0	1	2	3	4	5	6	7	8
----	----	----	----	----	---	---	---	---	---	---	---	---	---

IN-QUADRANT
ADDRESS ADL

FIG.29B

70	71	72	73	74	75	76	77	78	79	80	81	82	83
----	----	----	----	----	----	----	----	----	----	----	----	----	----

ADDRESS IN ONE
WAVELENGTH AD

FIG.29C

SECOND BIT AD1 FROM
THE BOTTOM IN ADDRESS
IN ONE WAVELENGTH SAME
LEAST SIGNIFICANT BIT AD0

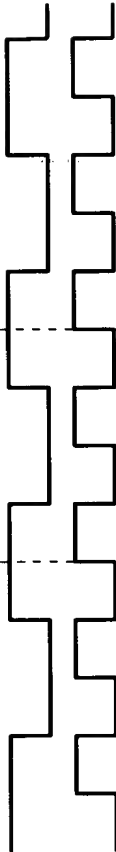


FIG.29D

TWO-PHASE
INCREASE AND
DECREASE PULSE
A PHASE
SIGNAL
B PHASE
SIGNAL

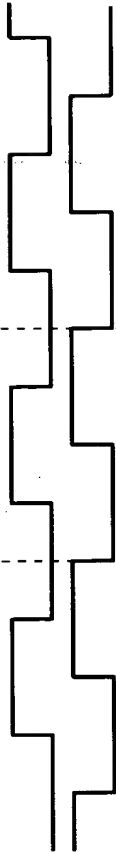


FIG.29E

5	6	0	1	2
---	---	---	---	---

IN-QUADRANT ADDRESS ADL
EXCLUDING LOWER 2 BITS

FIG.29F



IN-QUADRANT REFERENCE
ADDRESS SIGNAL

FIG.29G



INSIDE REFERENCE PULSE

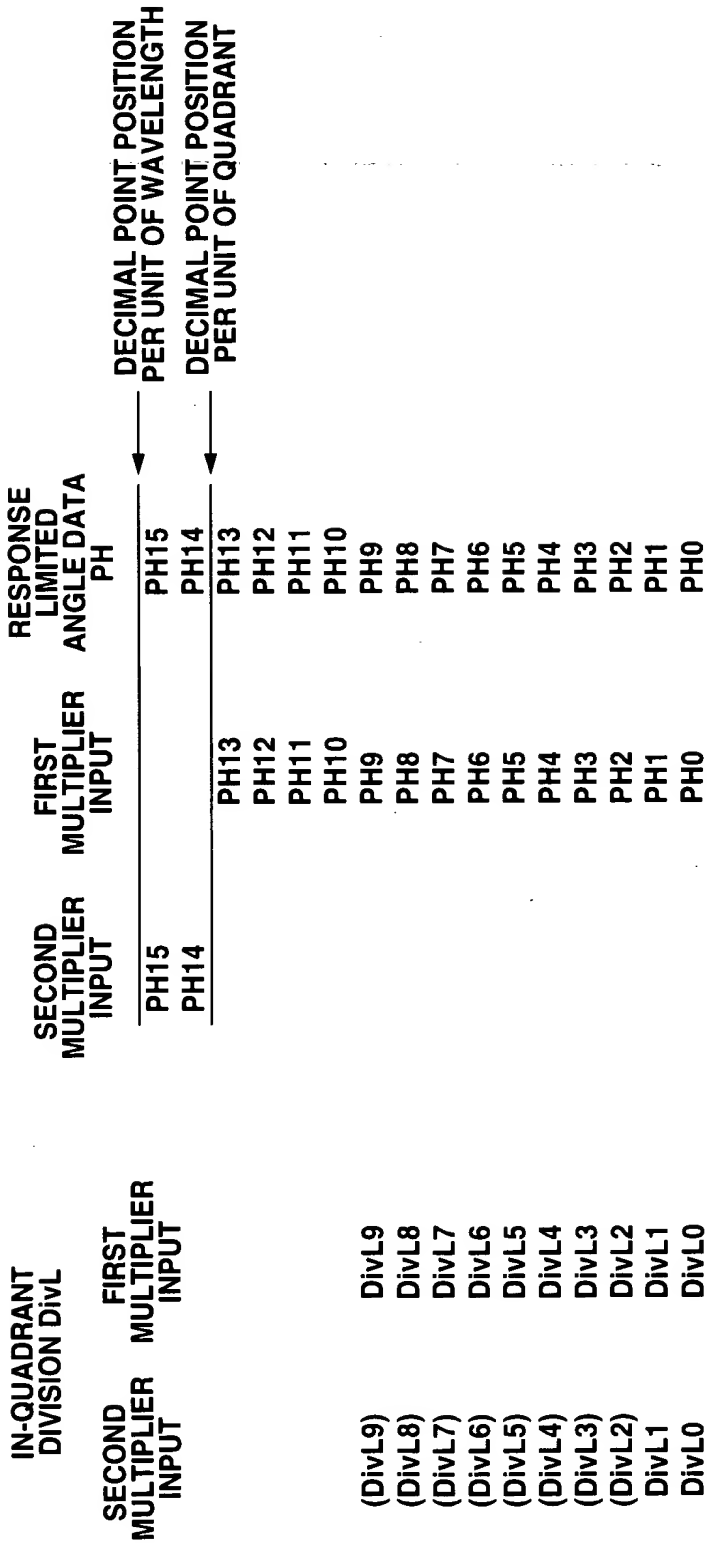


FIG.30A

FIG.30B

ONE WAVELENGTH DIVISION UNIT ADDRESS PD (ADDRESS IN ONE WAVELENGTH AD)=
CORRECTED ADDRESS PC +

IN-QUADRANT DIVISION UNIT ADDRESS PDL (IN-QUADRANT ADDRESS ADL)

OUTPUT OF CORRECTION ADDER	OUTPUT OF SECOND MULTIPLIER	OUTPUT OF SECOND MULTIPLIER
(PD25)=(AD11)	(PC25)=(AC11)	PDL23=ADL9
(PD24)=(AD10)	(PC24)=(AC10)	PDL22=ADL8
(PD23)=(AD9)	(PC23)=(AC9)	PDL21=ADL7
(PD22)=(AD8)	(PC22)=(AC8)	PDL20=ADL6
(PD21)=(AD7)	(PC21)=(AC7)	PDL19=ADL5
(PD20)=(AD6)	(PC20)=(AC6)	PDL18=ADL4
(PD19)=(AD5)	(PC19)=(AC5)	PDL17=ADL3
(PD18)=(AD4)	(PC18)=(AC4)	PDL16=ADL2
(PD17)=(AD3)	(PC17)=(AC3)	
(PD16)=(AD2)	(PC16)=(AC2)	
PD15=AD1	PC15=AC1	FOR INCREASE/DECREASE PULSE GENERATION
PD14=AD0	PC14=AC0	
(PD13)		PDL13
(PD12)		PDL12
(PD11)		PDL11
(PD10)		PDL10
(PD9)		PDL9
(PD8)		PDL8
(PD7)		PDL7
(PD6)		PDL6
(PD5)		PDL5
(PD4)		PDL4
(PD3)		PDL3
(PD2)		PDL2
(PD1)		PDL1
(PD0)		PDL0

DECIMAL POINT
POSITION IN
DIVISION ADDRESS

FIG.31

FIG.32A

QUADRANT DATA

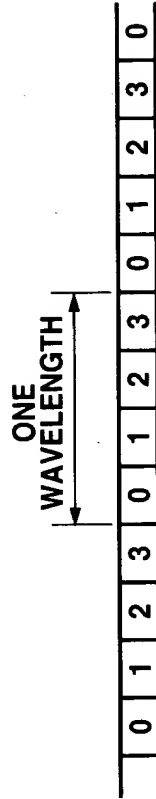


FIG.32B

REFERENCE
QUADRANT PULSE



FIG.32C

INSIDE
REFERENCE PULSE



FIG.32D

ORIGIN SIGNAL



FIG.32E

REFERENCE
ORIGIN PULSE

